

DS-03-007



January 12, 2004

To: Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

Subject: | Serial No. 10/618,535 07/11/03 |
Andreas Sibrai et al.
ENHANCED ARCHITECTURES OF VOLTAGE-
CONTROLLED OSCILLATORS WITH SINGLE
INDUCTOR (VCO-1L)
| _____ |

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation
In An Application.


The following Patents and/or Publications are submitted to
comply with the duty of disclosure under CFR 1.97-1.99 and
37 CFR 1.56.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450, on January 27, 2004.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

 1/27/04

"A Fully Integrated CMOS DCS-1800 Frequency Synthesizer," by Jan Craninckx et al., IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998, pp. 2054-2065, discusses a prototype frequency synthesizer for the DCS-1800 system which has been integrated in a standard 0.4um CMOS process without any external components.

U.S. Patent 5,937,340 to Philippe et al., "Integrated Oscillator and Radio Telephone Using Such an Oscillator," discusses an oscillator OSC intended to provide an output signal having a frequency which is variable as a function of a tuning voltage V_{tun} .

"Wide-Tuning-Range Dual-VCOs for Cable Modem Receivers," by Dari Stadius et al., 2002 IEEE MTT-S Digest, XP-001099558, IT-TU-53, pp. 581-584, proposes two dual-VCO implementations and a method for buffering the VCO signal without loading the LC-resonator.

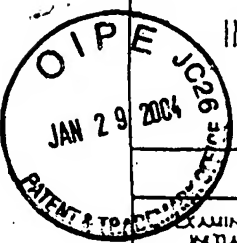
U.S. Patent 5,920,235 to Beards et al., "Voltage Controlled Oscillator Integrated Circuit," discusses VCOs manufactured using integrated circuit (IC) technology.

Sincerely,



Stephen B. Ackerman,
Reg. No. 37761

Form PTO-1449



INFORMATION DISCLOSURE CITATION
IN AN APPLICATION
(Use several sheets if necessary)

Docket Number (Optional) DS-03-007	Application Number 10/618,535
Applicant Andreas Sibrai et al.	
Filing Date 07/11/03	Group Art Unit

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
	5937340	8/10/99	Philippe et al.	455	310	10/7/97
	5920235	7/6/99	Beards et al.	331	108 D	6/25/97

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

- "A Fully Integrated CMOS DCS-1800 Frequency Synthesizer," by Jan Craninckx et al., IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, Dec. 1998, pp. 2054-2065.
- "Wide-Tuning-Range Dual-VCOs for Cable Modem Receivers," by Dari Stadias et al., 2002 IEEE MTT-S Digest, XP-001099558, IT-TU-53, pp. 581-584.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.